

MICRO-DISK SYSTEM ERRATTA AND ADDITIONAL INFORMATION

February 15, 1977

1. Capacitor C2 should be marked as 6.8uf (not 10uf) on the silk screen legend for the MDS controller board.
2. The following fix must be made to the MDS controller PC board in order to increase the reliability of reading data written using a different drive.
 - ✓ a. Cut traces to 8E pin 4 and 8E pin 5. These cuts may be made on the solder side. The connection 8E pin 3 to 8E pin 12 to 9F pin 14 should remain.
 - ✓ b. Add a jumper wire from 8E pin 4 to 9D pin 10.
 - ✓ c. Add a jumper wire from 8E pin 5 to 1E pin 12.
3. A 74LS157 may be substituted for a 74LS257 in location 2F of the controller PC board. Some kits will include a 74LS157 in place of a third 74LS257.
4. Interrupts during disk data transfer operations will cause data transfer errors. In systems where interrupts can occur, interrupts can be prevented by disabling interrupts before accessing the disk. The POLY-88 System with the 4.0 monitor causes continuous real time clock interrupts. This interrupt should be disconnected on the processor board to allow proper operation of the disk system.

Please send us feedback about the MICRO-DISK System documentation. We would like to make this documentation as complete, clear, and accurate as possible.

MICRO-DISK SYSTEM ERRATA AND ADDITIONAL INFORMATION

May 11, 1977

1. The current release number for the on board bootstrap PROM's is 3. The correct labeling of the three PROM's in a standard system is LE820-3, RE820-3 and SE8-1.
2. When reading and writing data files, BASIC fails to check that the file pointer is within the range of the file. Thus, BASIC programs must be very careful not to specify illegal data file transfers.
3. Interrupts during disk data transfer operations will cause data transfer errors. In systems where interrupts can occur, interrupts can be prevented by disabling interrupts before accessing the disk. The POLY-88 System with the 4.0 monitor causes continuous real time clock interrupts. This interrupt should be disconnected on the processor board to allow proper operation of the disk system.
4. Applying power to a disk drive while the ribbon cable is incorrectly plugged in to either the drive or the controller can DO DAMAGE TO THE DISK DRIVE! It is very important that the instructions given in the CABLE CONFIGURATION section of the MICRO DISK SYSTEM document be followed very carefully.
5. A few disk controller boards will exhibit intermittent failure as a result of glitches on the HUNT signal. If this problem exists, the glitches at HUNT can be observed on a scope. The problem can be fixed with the following modification to the disk controller board.
 - ✓ Cut trace connecting 9E pin 1 to 9E pin 15.
 - ✓ Add jumper wire connecting 9E pin 1 to 9F pin 7.
6. Disk drives now being delivered must be programmed somewhat differently than what is described in the DISK DRIVE CONFIGURATION section of the MICRO DISK SYSTEM document. The MUX trace which must be cut for multiple drive operation is now located on the header at 1F and is the strap labeled MX. The five straps that were labeled T1, T2, T3, T4, and T5 have been removed. The termination resistors are located at 1E. This resistor package should be removed in all drives except the one at the end of the ribbon cable. Be sure that the HS strap at 1F remains connected and the HM strap remains disconnected.

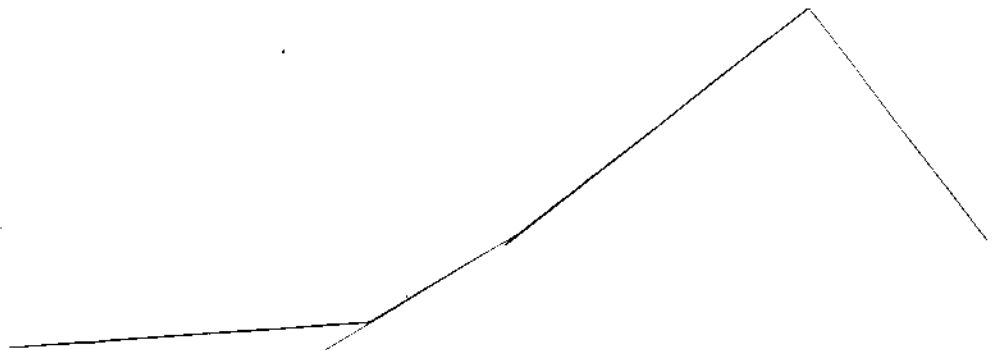
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MICRO-DISK ERRATA

JULY 26, 1977

When assembling the disk controller printed circuit boards labelled MDC-A3, the following modification must be made for correct operation:

- ✓ 1. On the soldering side, cut the trace connected to 9F pin 7.
- ✓ 2. On the soldering side, cut the trace connecting 9G pin 1 to 9E pin 15.
- ✓ 3. On the solder side, solder a wire between 9E pin 15 and 9F pin 7.
- ✓ 4. On the solder side, solder a wire between 9F pin 11 to 9G pin 1.



MICRO-DISK SYSTEM ERRATA AND ADDITIONAL INFORMATION

July 28, 1977

- ✓ 1. The current release number for the on board bootstrap PROM's is 3. The correct labeling of the three PROM's in a standard system is LE820-3, RE820-3 and SE8-1.
- ✓ 2. The power PC boards (revision 2) have been changed. The LM317K regulator has been replaced by a 7812 (or LM340-12) regulator. Also, the two resistors called R1 and R2 have been removed.
3. Interrupts during disk data transfer operations will cause data transfer errors. In systems where interrupts can occur, interrupts can be prevented by disabling interrupts before accessing the disk. The POLY-88 System with the 4.0 monitor causes continuous real time clock interrupts. This interrupt should be disconnected on the processor board to allow proper operation of the disk system.
4. Applying power to a disk drive while the ribbon cable is incorrectly plugged in to either the drive or the controller can DO DAMAGE TO THE DISK DRIVE! It is very important that the instructions given in the CABLE CONFIGURATION section of the MICRO DISK SYSTEM document be followed very carefully.
5. On page 28 of Micro-Disk System document the schematic should be changed so that 9G pin 1, 9E pin 15, 9D pin 11, and 9E pin 1 are labeled WINDOW/ instead of SP/.
6. The revision 3 controller boards have been modified to allow E800 to serve as a bootstrap address as well as E900. The schematic on page 27 should be modified so that 3E pin 14 and 3F pin 14 are connected to ground.
7. Disk drives now being delivered must be programmed somewhat differently than what is described in the DISK DRIVE CONFIGURATION section of the MICRO DISK SYSTEM document. The MUX trace which must be cut for multiple drive operation is now located on the header at 1F and is the strap labeled MX. The five straps that were labeled T1, T2, T3, T4, and T5 have been removed. The termination resistors are located at 1E. This resistor package should be removed in all drives except the one at the end of the ribbon cable. Be sure that the HS strap at 1F remains connected and the HM strap remains disconnected.

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