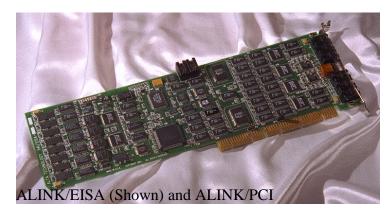
# **Alta Communications Products**



# ALINK Interfaces for PCI, PMC, and EISA

Scaleable Fibre Channel Communications



Board-level bus interface products for network processing, based upon compliance to the Fiber Channel Physical Layer Standard (FC-PH).

#### **Product Overview**

ALINK products from Alta Technology combine the best features of Fibre Channel communications with embedded computation on industry standard bus architectures. The ALINK is a Direct Memory Access (DMA) engine acting as a peripheral controller between one or more Fibre Channel transceivers and the host memory. The DMA engine directs communications traffic with minimal impact to the host processor. The 250 MBits/second tranceivers are bi-directional -- both directions supported by independent DMA engines, sustaining up to 50 Mbytes/second total throughput per ALINK over differential coax connections. Additionally, four 20 MBit/second asynchronous TLINKs are included for control and slow-speed communications.

ALINKs are available for PCI and PMC (PCI Mezzanine Card) and EISA busses, and are built into Alpha-based processors from Alta. Drivers for DEC, Sun, HP, SGI and Intel-based platforms support the ALINK in UNIX, VxWorks, and Win95 environments. The Fibre Channel transmitter/receiver IC integrates a complete phase-locked loop clock recovery and data retiming/regeneration subsystem, a phase locked loop clock synthesizer, a 10:1 Mux, a 1:10 Demux, an 8b/10b Encoder and a 10b/8b Decoder. It provides a complete physical interface in compliance with the Fibre Channel Physical Layer Standard (FC-PH). This implementation is supported in "raw" mode (with programmatic access to the full 250 MBits/second) or as the physical layer for TCP/IP communications between processors.



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#### **Specifications**

Bus Support	PCI	<i>PMC</i>	EISA	Alpha Processors
ALINKs per board	2	1	2	2 or 4
TLINKs	4	2	4	4

TLINKs are compatible with twisted pair SGS Thomson transputer products and embedded control products from Alta.

#### I/O Communications

ALINK Bandwidth (per direction) 250 Mbits/Sec per ALINK

Sustainable: 25 MBytes/Sec/ALINK/direction

TLINK Bandwidth (per direction) 20 Mbits/Sec per TLINK Sustainable: 1.7 MBytes/Sec

#### Cable Connections

ALINK - 6-pin (2X3 x .025" on .1 centers) to micro coaxial cable

TLINK - Modular handset jack to twisted pair

Electrical		PCI	<i>PMC</i>	<b>EISA</b>	Alpha Processors
DC voltage	5V- 5%				_
Power Dissipation	(Typical)	8	4	8	50 (Includes processor)
	(MAX)	11	6	11	76

#### **Environment**

Operating Temp.  $0^{0} - 60^{0} C$ 

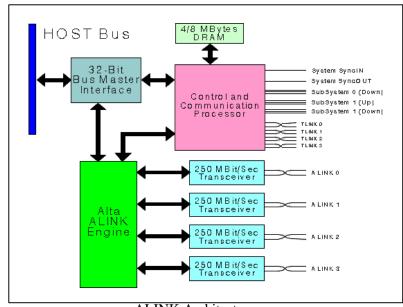
#### **Ordering Information**

To purchase: Please order:

for PCI ALINK/PCI
for PMC ALINK/PMC
for EISA ALINK/EISA
for Alpha see Alpha Processor Data Sheets

Other configurations may be available.

Please contact your Alta Sales Representative.



**ALINK Architecture** 

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