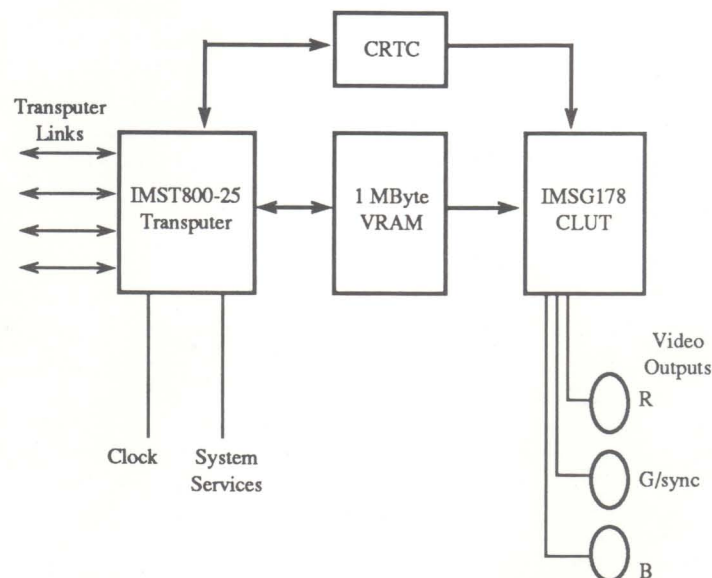


# Transtech TRAMs

## TTG1

### A TRANSPUTER GRAPHICS MODULE

- Features**
- ◆ IMST800-25 transputer
  - ◆ 1 MByte of video RAM
  - ◆ Supports resolutions upto 512 x 512 8-bit pixels
  - ◆ 256 colours from a maximum of 16.7 million
  - ◆ Software selectable 6 or 8-bit DAC mode
  - ◆ Software compatible with the Transtech TSB07 and Inmos IMSB007
  - ◆ Four serial transputer links
  - ◆ Industry standard size 2 TRAM
  - ◆ Compatible with Transtech range of TRAM motherboards



**Introduction** The Transtech TTG1 TRAM (TRANsputer Module) is a small industry standard daughterboard for the Transtech range of TRAM motherboards. The TTG1 consists of an IMST800-25 floating point transputer with 1 MByte of video memory suitable for user code, data and screen data.

#### Four Display Windows

The video memory can be treated as up to four independant display windows, each having a resolution of 512 x 512 8-bit pixels. Any one of these windows may be displayed at any time. User code may access any of the windows, including the one currently displayed, the access to the other three windows being invisible to the final display. The choice of display window can be controlled in software. The ability to switch active windows assists animation, where the next frame may be generated whilst the current frame is displayed.

**Display Resolutions**

The display resolution supported is 512 x 512 8-bit pixels, allowing up to 256 colours from a maximum of 16.7 million (via look up table) on the screen simultaneously. The TTG1 uses the Inmos IMSG178 colour palette device as the colour look up table, having three 6/8 bit DACs (Digital to Analog converters) on chip. To maintain upwards compatibility with the IMSB007 and TSB07, the TTG1 offers software selectable 6/8 bit DAC mode.

In 6-bit mode any 256 from 262144 colours may be displayed. In 8-bit mode, the display allows any 256 from 16.7 million colours.

Separate Red, Green and Blue SMB connectors are available for connection to standard RGB analogue monitors with composite sync on green.

**IMSB007 and TSB07 Compatible**

The Transtech TTG1 is compatible at software level with the Transtech TSB07 and Inmos IMSB007. A library of relevant routines required for setting up the video timing generator is supplied with the board. These library routines may be used to port existing IMSB007 or TSB07 code to the TTG1.

**TRAM Standard**

Measuring only 2.10" by 3.66" (5.33mm by 9.30mm) the TTG1 conforms to the published TRAM standard, allowing them to be easily plugged onto a wide range of motherboards for many different host machines. Up to ten TRAMs can be accommodated on a Transtech TMB08 board for IBM PC XT or AT's and compatibles, 4 on the Transtech TMB04 and TMB05, 16 on a TMB12 double extended eurocard and 32 on the MCP1000 Multi Computing Platform for Sun workstations, allowing rapid prototyping of transputer systems. Transtech TRAMs are also compatible with motherboards from other manufacturers. Further details on the TRAM standard and TRAM Module Motherboard Architecture are published by Prentice Hall in 'Transputer Technical Notes' ISBN 0-130929126-1.

**Software**

The TTG1 is supported by Occam primitives which generate key graphics functions callable from the Occam TDS or 3L's scientific languages.

**Ordering Information**

PART NUMBER	DESCRIPTION
TTG1	TRANSPUTER GRAPHICS TRAM WITH T800-25 AND IMSG178



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Document Reference: TTG1FLY0789