

TTM

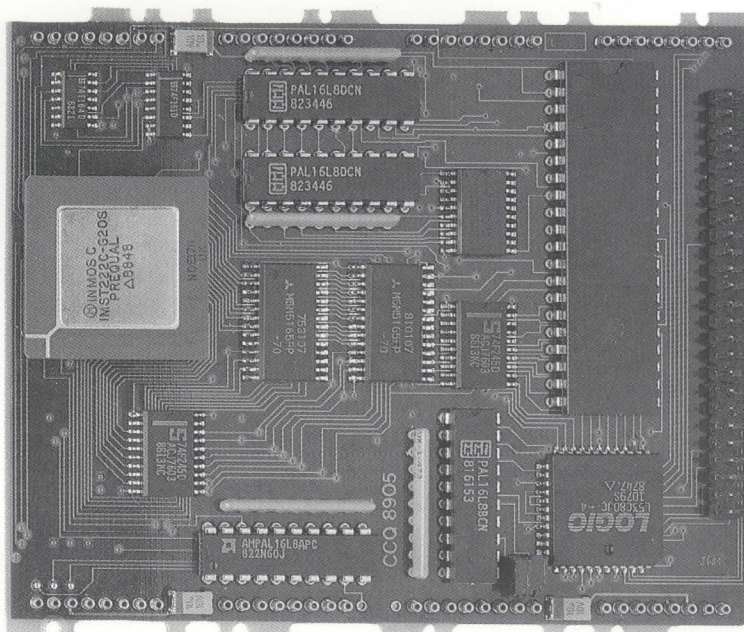
Transtech TRAMs TTM11

TTM

A SCSI INTERFACE TRANSPUTER MODULE

Features

- ◆ IMST222 16-bit transputer
 - ◆ 1.8 MBytes/sec data rate over SCSI bus
 - ◆ Extendable to multi master, multi slave system
 - ◆ Software support with TBIOS disk filing system
 - ◆ Industry standard size 4 TRAM
 - ◆ Compatible with Transtech range of TRAM motherboards
-



Introduction The Transtech TTM11 is a small daughterboard for the Transtech range of TRAM (TRANsputer Module) motherboards. It comprises an IMST222 16-bit transputer with 64K of fast static RAM interfaced to a Logic Devices SCSI controller. Data throughput is determined by a single Inmos link bandwidth, typically of the order of 1.2 MBytes/sec, although the SCSI bus side of the module will transfer at data rates of upto 1.8 MBytes/sec.

TRAM Standard

Measuring only 4.20" by 3.66" (10.67mm x 9.30mm) the TTM11 conforms to the published TRAM standard, allowing it to be plugged easily onto a wide range of motherboards for many different host machines. Up to 10 TRAMs can be accommodated on a Transtech TMB08 board for IBM PC XT or AT's and compatibles, 4 on the Transtech TMB04 and TMB05, 16 on a TMB12 double extended eurocard and 32 on the MCP1000 Multi Computing Platform for Sun workstations, allowing rapid prototyping of transputer systems. The TTM11 is compatible with TRAM motherboards from Transtech and also those from other manufacturers. Further details on the TRAM standard and TRAM motherboard architecture are published by Prentice Hall in 'Transputer Technical Notes' ISBN 0-130929126-1.

Software TBIOS (Transputer BIOS) is a library of Occam routines provided to access the SCSI bus from an application program running on a network of transputers.

Software developers using Occam, can include the TBIOS as a library function to provide fast and efficient access to mass storage on the SCSI bus. Alternatively the use of TDOS which comprises the TBIOS routines provides a shell from which to run applications such as the TDS (Transputer Development System). Full listings of Occam examples to interface to the TBIOS are provided.

Users of the 3L scientific languages can run TBIOS as a separate task, and use channel communication from their application to access the mass store directly, without going through layers of unwieldy protocols. Examples are provided in each language to emulate the required primitives open, read, write and close file structured I/O. These primitives are implemented in as near a 'look alike' form as the inherent language primitives to allow simple mass editing of existing code to run under this system.

A RAMDISK utility provided with the TBIOS allows the user to create automatic caching of files to memory without major program changes.

EPROM Bootable The TTM11 can be used to boot a network of transputers from the SCSI port when used in conjunction with the TTM12 EPROM loader TRAM which boots the T222 transputer. For more details see information on the Transtech TTM12 TRAM.

PART NUMBER	DESCRIPTION
TTM11	TRANSPUTER SCSI INTERFACE MODULE



TRANSTECH DEVICES LIMITED
Unit 17, Wye Industrial Estate
London Road
High Wycombe
Buckinghamshire
HP11 1LH
England
Telephone: [+44] 0494 464303
Facsimile: [+44] 0494 463686

©Copyright Transtech Devices Limited 1989

Transtech has a policy of continuous development and reserves the right to change these specifications without prior warning. Transtech cannot accept responsibility to any third party for loss or damage arising from the use of this information. Transtech acknowledges all registered trademarks

Document Reference: TTM11FLY0789